AGILENT TECHNOLOGIES, INC. Legal Department, DL429 Intellectual Property Administration P. O. Box 7599 Loveland, Colorado 80537-0599



## ATTORNEY DOCKET NO. 10020707-1 <

Examiner: Pham, Thanh V.

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): John F. Casey, et al.

Serial No.: 10/600,143

Filing Date: June 19, 2003 Group Art Unit: 2823

Title: METHODS FOR MAKING MICROWAVE CIRCUITS

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria VA 22313-1450
Sir: INFORMATION DISCLOSURE STATEMENT
This Information Disclosure Statement is submitted:
(a) Under 37 CFR 1.97(b). (Within three months of filing national application; or date of entry of national application; or before mailing date of first Office action on the merits; whichever occurs last).
(b) Under 37 CFR 1.97(c) together with <i>either</i> a:  Statement under CFR 1.97(e), or  \$180.00 fee under 37 CFR 1.17(p).  (After the CFR 1.97(b) time period, but before a final action or notice of allowance, whichever occurs first).
(c) Under 37 CFR 1.97(d) together with: a Statement under 37 CFR 1.97(e), and \$180.00 fee as set forth in 37 CFR 1.17(p). (After a final action or notice of allowance, whichever occurs first, but before payment of the issue fee).
PRIOR APPLICATIONS  References identified with an asterisk (*) in the enclosed PTO Form 1449, were disclosed in prior Patent Application No. , filed , now U.S. Patent No. , and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).
FOREIGN LANGUAGE DOCUMENTS  A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56(c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

04/28/2005 EFLORES 00000026 501078 10600143

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Rev 10/04 (IDS)

### ATTORNEY DOCKET NO. 10020707-1

Please charge to Deposit Account <b>50-1078</b> the sum of this application, please charge any fees required or credi pursuant to 37 CFR 1.25.	n of \$180.00 . At any time during the pendency t any overpayment to Deposit Account <b>50-1078</b>
	Respectfully submitted,
I hereby certify that this correspondence is being deposited with the United States Postal Service as:	John F. Casey, et al.
First Class Mail	Gregory W. Osterloth
"Express Mail Post office to Addressee" service under 37 CFR 1.10.	Attorney/Agent for Applicant(s)
Express Mai Label No.	Reg. No. 36,232
Date of Deposit: April 25, 2005	Date: April 25, 2005
In an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.	Telephone No. (303) 291-3200
OR	
☐ I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.	
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### Form PTO-1449

# LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTORNEY DOCKET NO. 10020707-1 10/600,143

APPLICANT

John F. Casey, et al.

(Use several sheets if necessary)

John F. Casey, et al.

FILING DATE
June 19, 2003

GROUP
2823

### REFERENCE DESIGNATION

### **U.S. PATENT DOCUMENTS**

EXAMINER * INITIAL	DOCUMENT NUMBER	DATE	NAME
	4,808,274	02-1989	Nguyen, Pascaline H.
	4,303,480	12-1981	Wood et al.
	4,806,941	02-1989	Knochel et al.
	2001/0012692	08-2001	Miller et al.

### **FOREIGN PATENT DOCUMENT**

DOCUMENT NUMBER	DATE	NAME	TRANSLATIO YES NO

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

Wolf, et al., "Silicon Processing for the VLSI ERA", Volume1: Process Technology, Lattice Press, 1986, pages 531-535.

DATE CONSIDERED

Copies of these references are not enclosed Pursuant to 37 CFR 1.98(d). (See accompanying IDS)

**EXAMINER**